

VP077

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s):	Atousa Soroushi	Art Unit:	2185
Serial No.:	10/758,432	Examiner:	Jae Un Yu
Filed:	January 14, 2004	Confirmation No.:	5658
Title:	Method And Apparatus For High Speed Addressing Of Memory Locations Within The Same Page		

STATUS REQUEST

Sir:

It is respectfully requested that Applicant(s) attorneys be advised of the current status of the above-identified application.

Respectfully submitted,

/Mark P. Watson/

Mark P. Watson

Registration No. 31,448

Please address all correspondence to:
Epson Research and Development, Inc.
Intellectual Property Department
2580 Orchard Parkway, Suite 225
San Jose, CA 95131
Customer No. 20178
Phone: (408) 952-6124
Facsimile: (408) 954-9058
Date: October 6, 2006